



Indira Gandhi Delhi Technical University For Women
(Established by Govt. of Delhi vide Act 09 of 2012)
Kashmere Gate, Delhi-110006
Department of Electronics and Communication Engineering



Two days student workshop on Cadence Virtuoso Software
(20th August 2019 -21th August 2019)

Department of Electronics and Communication, along with IEEE IGDTUW Student Branch and SMDP C2SD at IGDTUW, organized a Two days student workshop on Cadence Virtuoso software from 20-21 August, 2019 in the VLSI Lab, IGDTUW campus. In this workshop, the students were given an introduction to the analog CMOS design flow. The workshop gave the students an insight into the basics of MOSFETs and the various tools used for verification. The participants of the workshop got an overview of analog design analysis and simulation, low power and nanoscale VLSI design. To excite students about analog circuit design, they were given a hands-on introduction to the Cadence Design Environment. This was primarily attended by M.Tech. VLSI Design (1st year) students.



Dr Nidhi Goel
(Coordinator)
Professor
Department of ECE

